

### General Description

The MIC94066-69 are dual high-side load switches designed for operation between 1.7V to 5.5V. The devices contain a pair of low on-resistance, 115mΩ(max) P-channel MOSFETs that support over 2A of continuous current. The MIC94067 and MIC94069 feature an active load discharge circuit which insures capacitive loads retain no charge when the main switch is in an OFF state.

An active pull-down on the enable input keeps MIC94066-69 in a default OFF state until the EN pin is pulled to a high level. The built-in level shift circuitry allows for a logic signal that may be different from the supply voltage to switch the high-side P-channel MOSFET on or off.

MIC94066-67 feature rapid turn on while MIC94068-69 provide a slew rate controlled Soft-Start turn-on of 800μs (typical) to prevent in-rush current from glitching supply rails.

MIC94066-69's voltage range makes them suitable for 1-cell Lithium ion and 2- to 3-cell NiMH/NiCad/Alkaline powered systems, as well as all 5V applications. Their low operating current of 2μA and low shutdown current of <1μA maximize battery life.

Data sheets and support documentation can be found on Micrel's web site at [www.micrel.com](http://www.micrel.com).

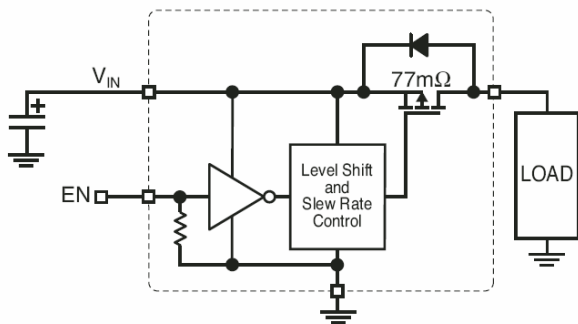
### Features

- 1.7V to 5.5V input voltage range
- 2A continuous operating current
- 85mΩ (typ) R<sub>ON</sub>
- Built-in level shift for control logic; can be operated by 1.5V logic.
- Low 2μA quiescent current
- Soft-Start: MIC94068-69
- Micro-power shutdown <1μA
- Load discharge circuit: MIC94067, MIC94069
- Space saving 2mm x 2mm MFL™

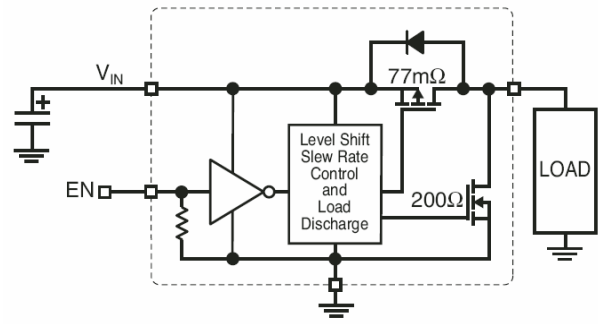
### Applications

- Load switch in portable applications:
  - Cellular phones
  - PDAs
  - MP3 players
  - Digital Cameras
  - Portable instrumentation
- Battery switch-over circuits
- Level translators

### Typical Application



½ MIC94066, 68  
Load Switch Application



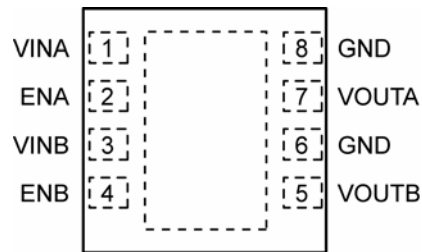
½ MIC94067, 69  
Load Switch with Capacitive Load Discharge

### Ordering Information

Part Number	Part Marking*	Soft-Start	Load Discharge	Pb-Free Package
MIC94066YML	P66			2x2 mm MLF™
MIC94067YML	P67		•	
MIC94068YML	P68	•		
MIC94069YML	P69	•	•	

\* Note: Over bar symbol may not be to scale

### Pin Configuration



Top View  
2x2 mm MLF™ (code)

### Pin Description

Pin Number	Pin Name	Pin Function
1	V <sub>IN A</sub>	Source of P-channel MOSFET.
2	EN A	Enable (Input): Active-high CMOS compatible control input for switch A. Do not leave floating.
3	V <sub>IN B</sub>	Source of P-channel MOSFET.
4	EN B	Enable (Input): Active-high CMOS compatible control input for switch A. Do not leave floating.
5	V <sub>OUT B</sub>	Drain of P-channel MOSFET.
6	GND	Ground. Both ground pins must be grounded.
7	V <sub>OUT A</sub>	Drain of P-channel MOSFET.
8	GND	Ground. Both ground pins must be grounded.

### Absolute Maximum Ratings<sup>(1)</sup>

Input Voltage ( $V_{IN}$ )	+6V
Enable Voltage ( $V_{EN}$ )	+6V
Continuous Drain Current ( $I_D$ ) <sup>(3)</sup>	
$T_A = 25^\circ\text{C}$	$\pm 2\text{A}$
$T_A = 85^\circ\text{C}$	$\pm 1.4\text{A}$
Pulsed Drain Current ( $I_{DP}$ ) <sup>(4)</sup>	$\pm 6\text{A}$
Continuous Diode Current ( $I_S$ ) <sup>(4)</sup>	-50mA
Storage Temperature ( $T_S$ )	-55°C to +150°C
EDS Rating – HBM <sup>(6)</sup>	4KV

### Operating Ratings<sup>(2)</sup>

Input Voltage ( $V_{IN}$ )	+1.7 to +5.5V
Junction Temperature ( $T_A$ )	-40°C to +125°C
Package Thermal Resistance	
2x2 MLF ( $\Theta_{JA}$ )	90°C/W
2x2 MLF ( $\Theta_{JC}$ ) <sup>(3)</sup>	45°C/W

### Electrical Characteristics

$V_{IN} = 5\text{V}$ ;  $T_A = 25^\circ\text{C}$ , bold values indicate  $-40^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$ , unless noted.

Symbol	Parameter	Condition	Min	Typ	Max	Units
$V_{EN\_TH}$	Enable Threshold Voltage	$V_{IN} = 1.8\text{V to } 4.5\text{V}, I_D = -250\mu\text{A}$	0.5		1.2	V
		$V_{IN} = 1.7\text{V to } 4.5\text{V}, I_D = -250\mu\text{A}$	0.4		1.2	V
$I_{EN}$	Enable Input Current	$V_{IN} = V_{EN} = 5.5\text{V}$		2	4	$\mu\text{A}$
$I_{VIN}$	OFF State Leakage Current	$V_{IN} = +5.5\text{V}, V_{EN} = 0\text{V}$			1	$\mu\text{A}$
$R_{DS(ON)}$	P-Channel Drain to Source On Resistance	$V_{IN} = +4.5\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$		85	115	m $\Omega$
		$V_{IN} = +3.6\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$		100	140	m $\Omega$
		$V_{IN} = +2.5\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$		145	200	m $\Omega$
		$V_{IN} = +1.8\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$		155	215	m $\Omega$
		$V_{IN} = +1.7\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$		165	225	m $\Omega$
$R_{SHUTDOWN}$	Turn-off Impedance	$V_{IN} = +3.6\text{V}, I_{TEST} = 1\text{mA}, V_{EN} = 0\text{V}$ MIC94067, 69		200	300	$\Omega$

### Dynamic

Symbol	Parameter	Condition	Min	Typ	Max	Units
$t_{ON\_DLY}$	Turn-On Delay Time	$V_{IN} = +3.6\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$ MIC94066, 67		0.85	1.5	$\mu\text{s}$
		$V_{IN} = +3.6\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$ MIC94068, 69		700	1200	$\mu\text{s}$
$t_{ON\_RISE}$	Turn-On Rise Time	$V_{IN} = +3.6\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$ MIC94066, 67	0.5	1	5	$\mu\text{s}$
		$V_{IN} = +3.6\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$ MIC94068, 69	500	800	1500	$\mu\text{s}$
$t_{OFF\_DLY}$	Turn-Off Delay Time	$V_{IN} = +3.6\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$ MIC94066, 67		115	200	ns
		$V_{IN} = +3.6\text{V}, I_D = -100\text{mA}, V_{EN} = 1.5\text{V}$ MIC94068, 69		100	200	ns

**Dynamic (cont.)**

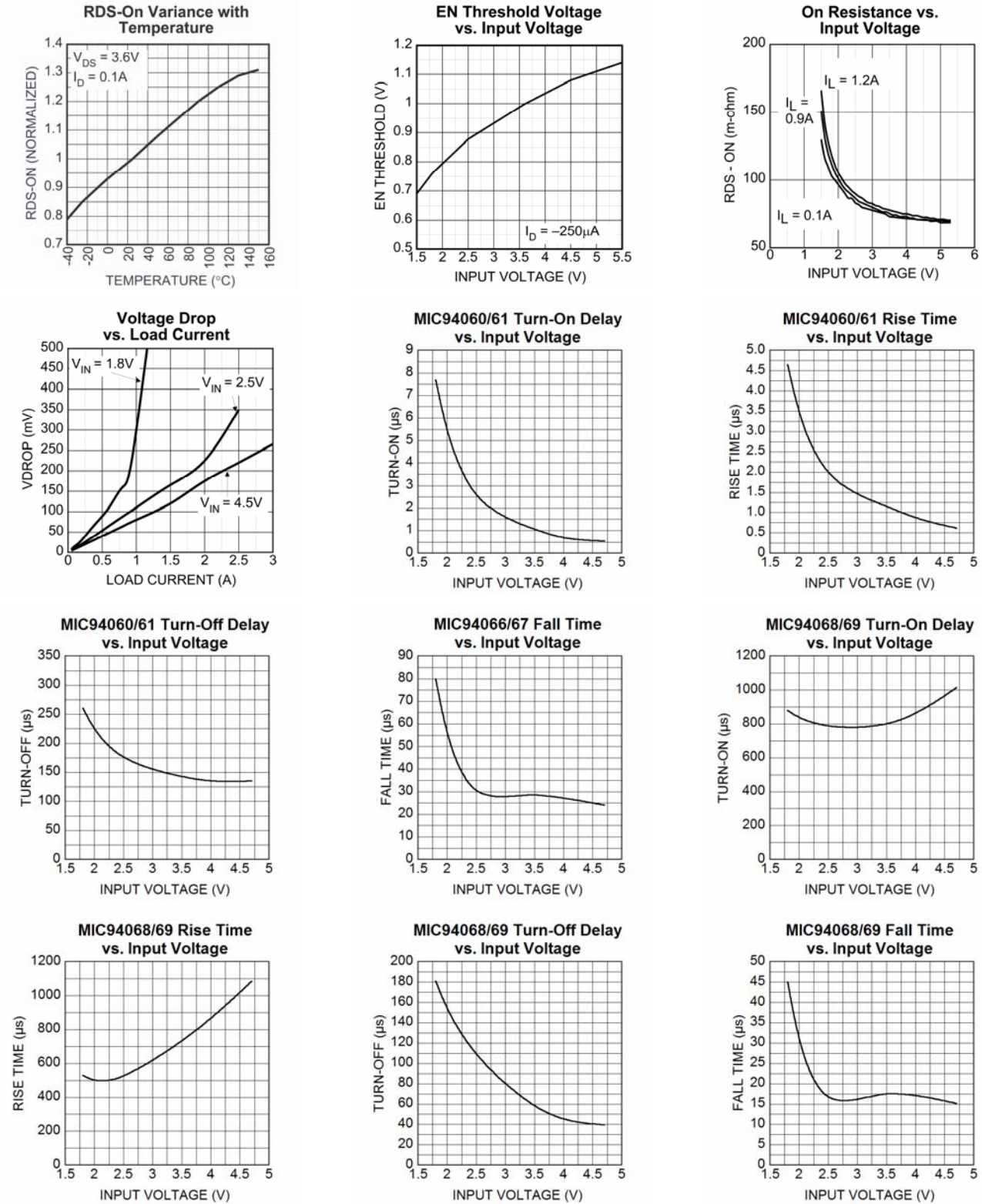
$t_{\text{OFF\_FALL}}$	Turn-Off Fall Time	$V_{\text{IN}} = +3.6\text{V}$ , $I_{\text{D}} = -100\text{mA}$ , $V_{\text{EN}} = 1.5\text{V}$ MIC94066, 67	60	100	ns
		$V_{\text{IN}} = +3.6\text{V}$ , $I_{\text{D}} = -100\text{mA}$ , $V_{\text{EN}} = 1.5\text{V}$ MIC94068, 69	60	100	ns

## Notes:

1. Exceeding the absolute maximum rating may damage the device.
2. The device is not guaranteed to function outside its operating rating.
3. With backside thermal contact to PCB.
4. Pulse width  $< 300\mu\text{s}$  with  $< 2\%$  duty cycle.
5. Continuous body diode current conduction (reverse conduction, i.e.  $V_{\text{OUT}}$  to  $V_{\text{IN}}$ ) is not recommended.
6. Devices are ESD sensitive. Handling precautions recommended. HBM (Human body model), 1.5k in series with 100pF.

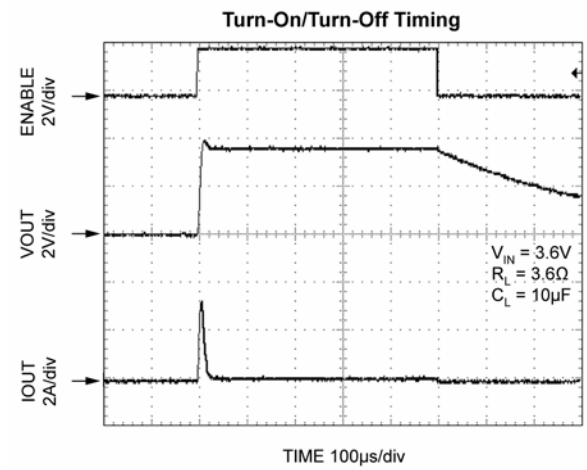
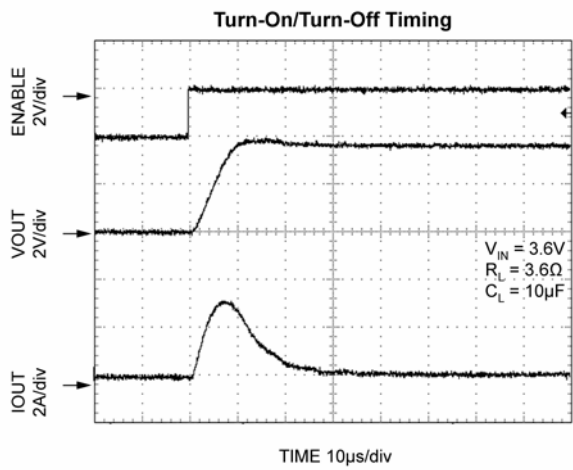
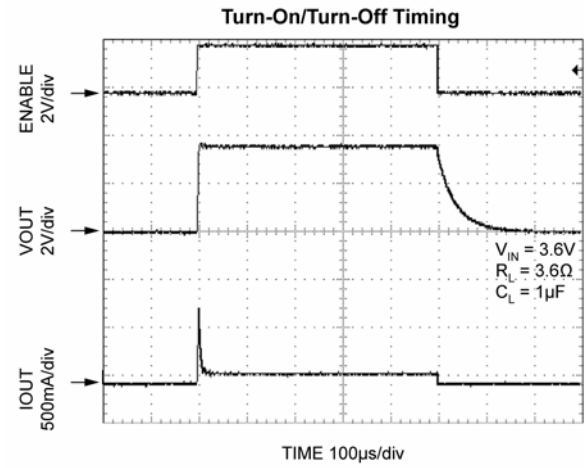
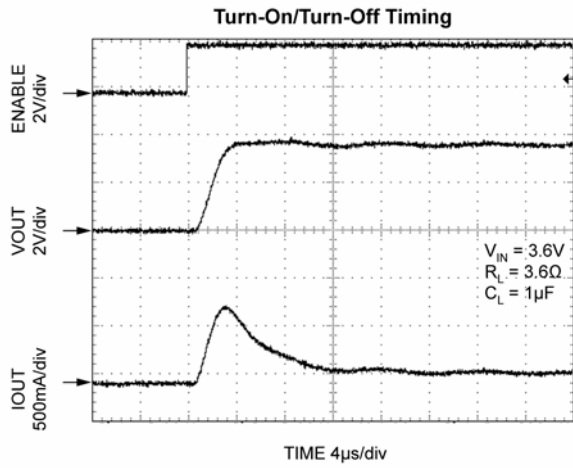
## Typical Characteristics

$R_L = 100\text{mA}$ ,  $C_L = 0\mu\text{F}$  for the following plots

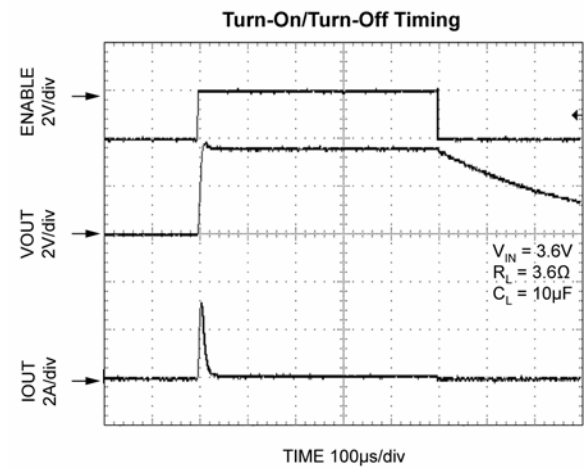
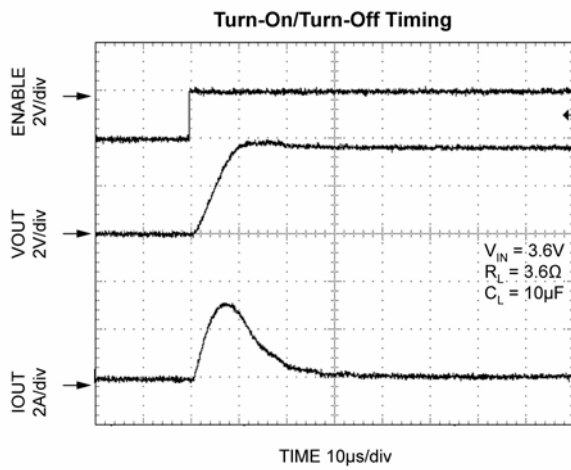
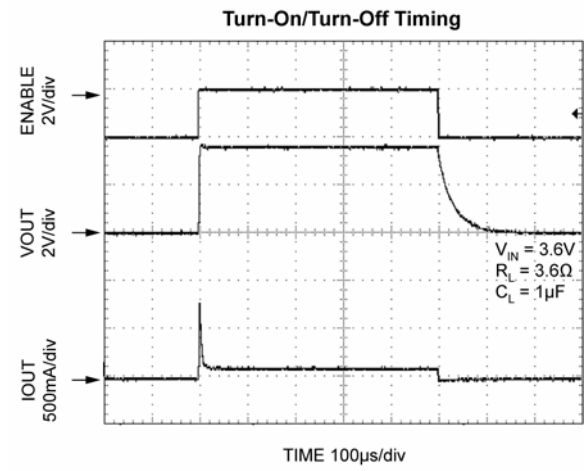
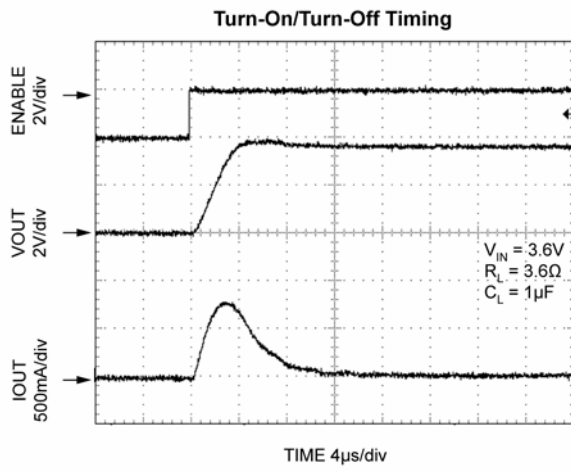


# Functional Characteristics

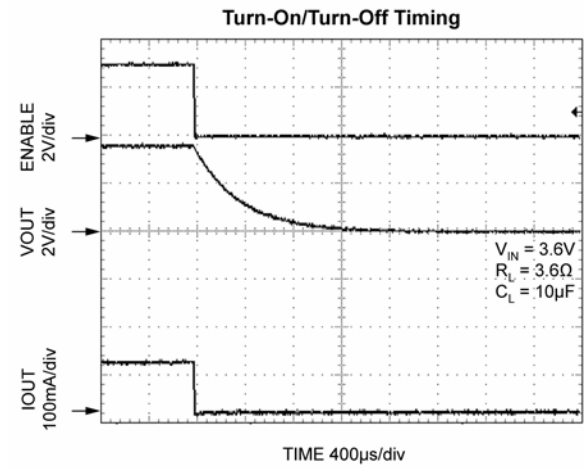
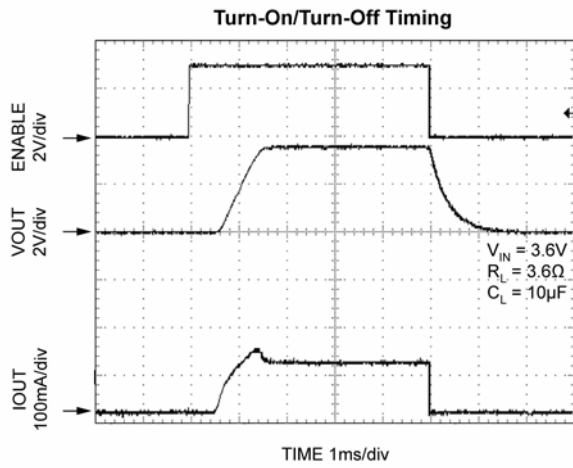
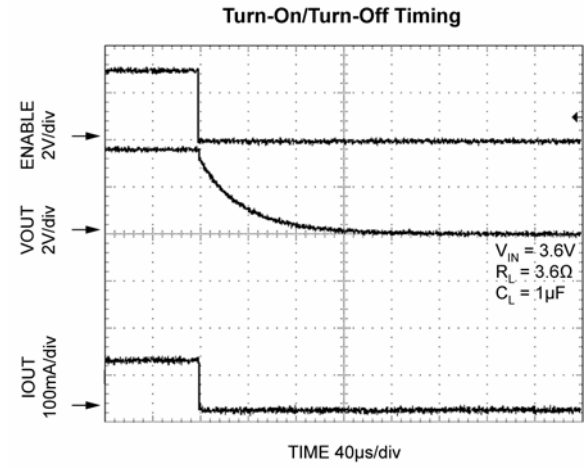
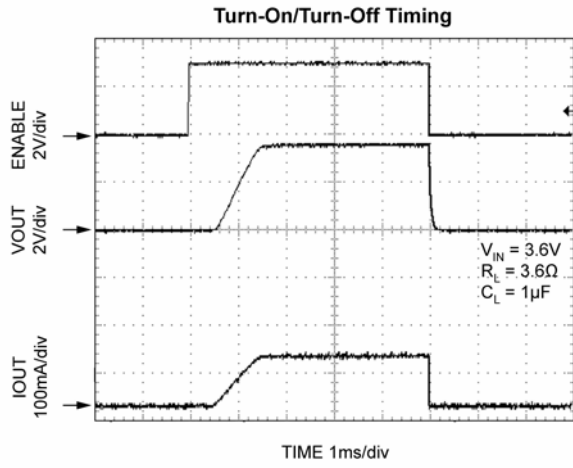
## MIC94066



# MIC94067

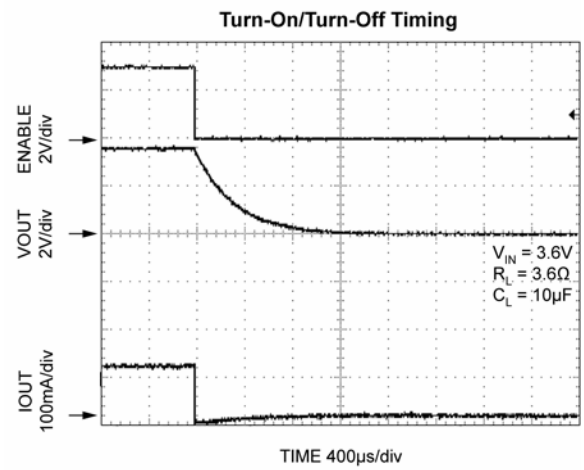
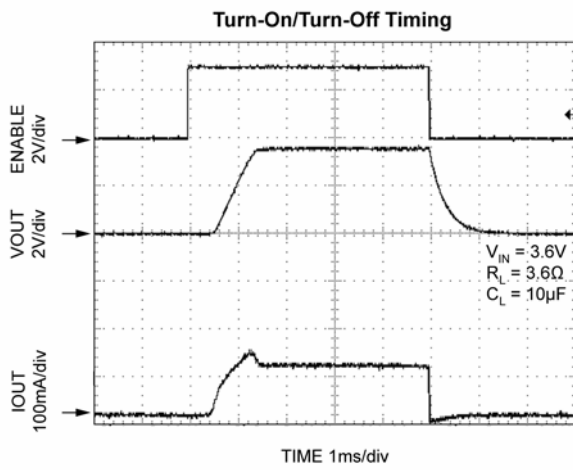
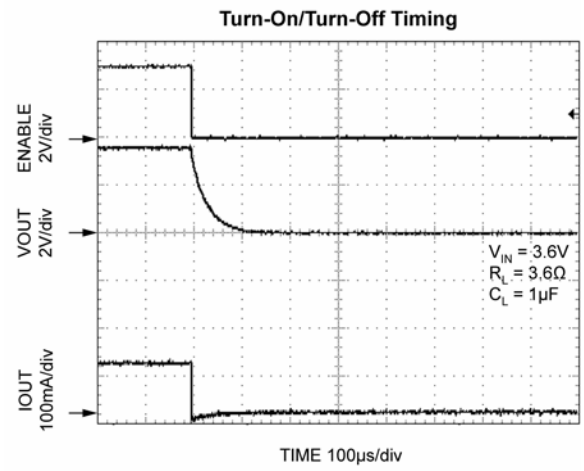
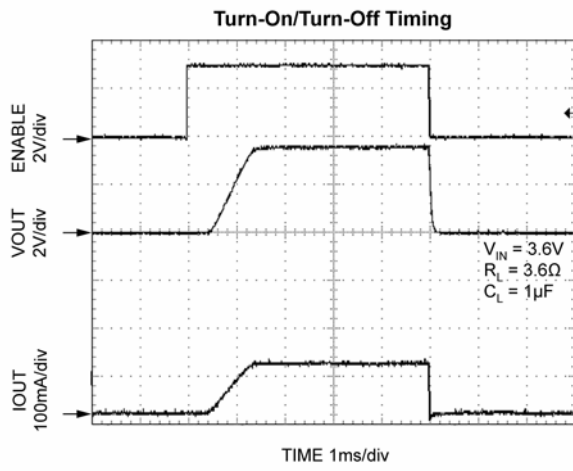


# MIC94068

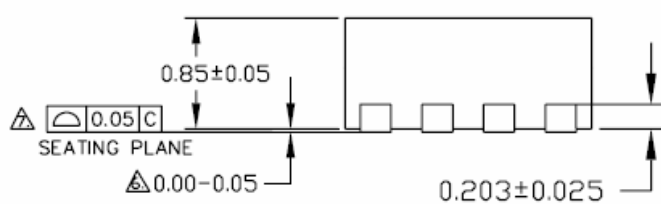
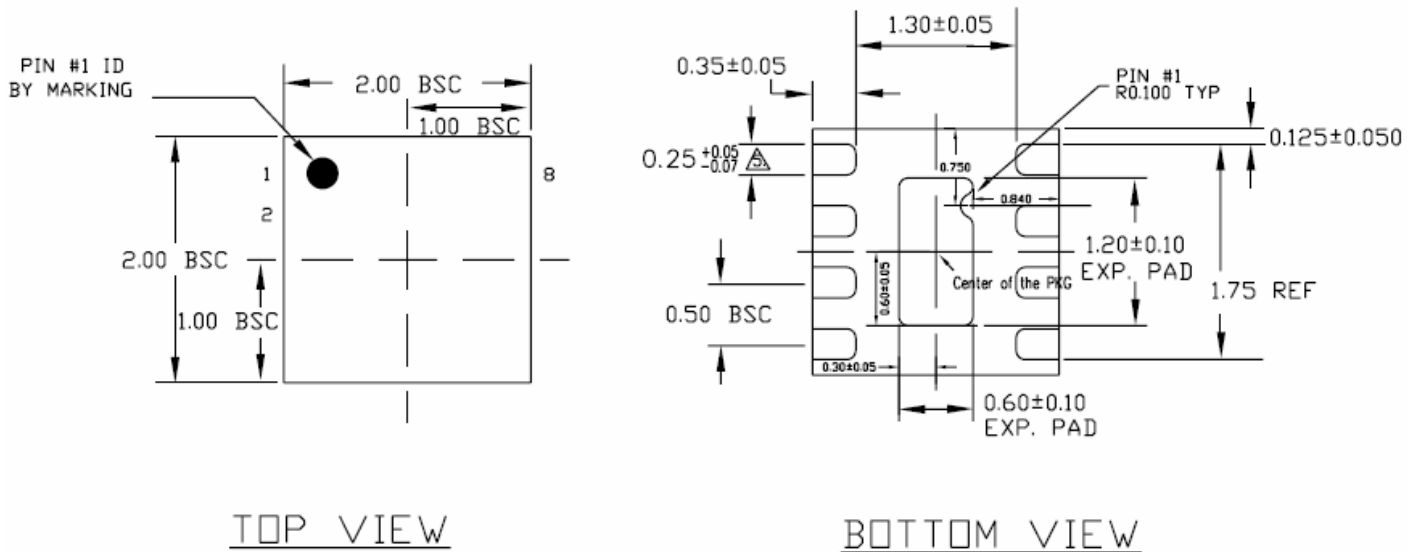




# MIC94069



### Package Information



- NOTE:
1. ALL DIMENSIONS ARE IN MILLIMETERS.
  2. MAX. PACKAGE WARPAGE IS 0.05 mm.
  3. MAXIMUM ALLOWABLE BURRS IS 0.076 mm IN ALL DIRECTIONS.
  4. PIN #1 ID ON TOP WILL BE LASER/INK MARKED.
- △ DIMENSION APPLIES TO METALIZED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25 mm FROM TERMINAL TIP.
- △ APPLIED ONLY FOR TERMINALS.
- △ APPLIED FOR EXPOSED PAD AND TERMINALS.

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