



## DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C		
60V	7.5Ω @ V <sub>GS</sub> = 5V	0.23A		

## Description

This MOSFET has been designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- Motor Control
- Power Management Functions

## Features

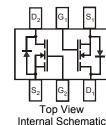
- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3 & 4)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

- Case: SOT363
- Case Material: Molded Plastic. "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approximate)



Top View



Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
2N7002DW-7-F	Standard	SOT363	3,000/Tape & Reel
2N7002DWQ-7-F	Automotive	SOT363	3,000/Tape & Reel
2N7002DW-13-F	Standard	SOT363	10,000/Tape & Reel
2N7002DWQ-13-F	Automotive	SOT363	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

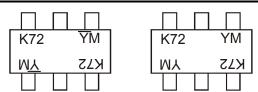
 See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb<sub>2</sub>O<sub>3</sub> Fire Retardants.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



K72 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)  $\overline{Y}M$  = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or  $\overline{Y}$  = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

Notes:

Date Coue Rey															
Year	1998	1999	2000	2001	2002	2003	2004		2011	2012	2013	2014	2015	2016	2017
Code	J	К	L	М	Ν	Р	R		Y	Z	А	В	С	D	E
Month	lan	Fel	h   1	Mar	Anr	Mav	1		Jul	Aug	Son	Oc	4	Nov	Dec
Wonth	Jan	rei	0 1	viai	Apr	iviay	Jui	1	Jui	Aug	Sep	00	L	VOV	Dec
Code	1	2		3	4	5	6		7	8	9	0		Ν	D



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage		V <sub>DSS</sub>	60	V	
Drain-Gate Voltage $R_{GS} \le 1.0 M\Omega$		V <sub>DGR</sub>	60	V	
Cate Source Veltage		Continuous	V <sub>GSS</sub>	±20	V
Gate-Source Voltage		Pulsed	V <sub>GSS</sub>	±40	V
Continuous Drain Current (Note 7) $V_{GS}$ = 5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C T <sub>A</sub> = +100°C	ID	0.23 0.18 0.14	А
Maximum Continuous Body Diode Forward Curren	nt (Note 7)	Is	0.53	А	
Pulsed Drain Current (10µs pulse, duty cycle = 1%	<b>6</b> )	I <sub>DM</sub>	0.8	А	

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
	T <sub>A</sub> = +25°C		0.31	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	PD	0.2	W
	T <sub>A</sub> = +100°C		0.12	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	$R_{ ext{ heta}JA}$	410	°C/W
	T <sub>A</sub> = +25°C		0.4	
Total Power Dissipation (Note 7)	T <sub>A</sub> = +70°C	PD	0.25	W
	T <sub>A</sub> = +100°C		0.15	
Thermal Resistance, Junction to Ambient (Note 7)	Steady state	$R_{ ext{ heta}}JA$	318	°C/W
Thermal Resistance, Junction to Case (Note 7)	Steady state	$R_{ ext{ heta}JC}$	135	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)								
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	60	70		V	$V_{GS}$ = 0V, $I_{D}$ = 10µA	
Zero Gate Voltage Drain Current	@ T <sub>C</sub> = +25°C @ T <sub>C</sub> = +125°C	I <sub>DSS</sub>	—		1.0 500	μA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	
Gate-Body Leakage		I <sub>GSS</sub>	_		±10	nA	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	
ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage		V <sub>GS(th)</sub>	1.0		2.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	@ T <sub>J</sub> = +25°C		_	3.2	7.5	Ω	$V_{GS}$ = 5.0V, $I_{D}$ = 0.05A	
	@ T <sub>J</sub> = +125°C			4.4	13.5		V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.5A	
On-State Drain Current		I <sub>D(ON)</sub>	0.5	1.0		A	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 7.5V	
Forward Transconductance		<b>g</b> fs	80			mS	V <sub>DS</sub> =10V, I <sub>D</sub> = 0.2A	
Diode Forward Voltage		V <sub>SD</sub>		0.78	1.5	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 115mA	
DYNAMIC CHARACTERISTICS (Note 9)								
Input Capacitance		Ciss	_	22	50	pF		
Output Capacitance		Coss	_	11	25	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V f = 1.0MHz	
Reverse Transfer Capacitance		Crss	_	2.0	5.0	pF		
SWITCHING CHARACTERISTICS (Note 9)								
Turn-On Delay Time		t <sub>D(on)</sub>	_	7.0	20		V <sub>DD</sub> = 30V, I <sub>D</sub> = 0.2A,	
Turn-Off Delay Time		t <sub>D(off)</sub>	_	11.0	20	ns	$R_{L} = 150\Omega, V_{GEN} = 10V,$ $R_{GEN} = 25\Omega$	

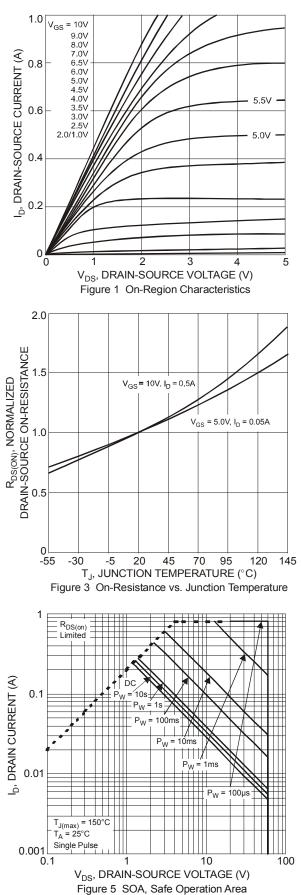
Notes:

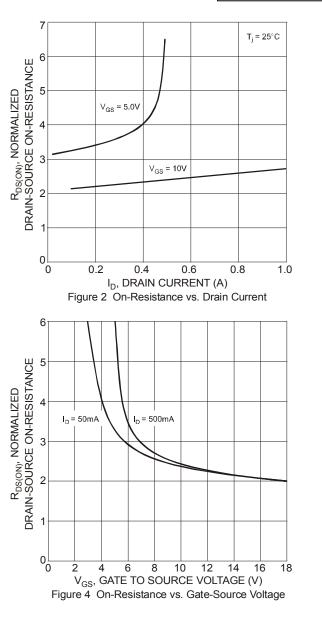
Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



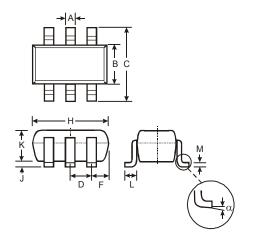






## **Package Outline Dimensions**

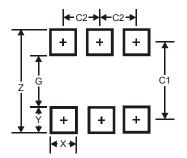
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT363							
Dim	Min Max						
Α	0.10	0.30					
В	1.15 1.35						
С	2.00	2.20					
D	0.65	Тур					
F	0.40	0.45					
Н	1.80	2.20					
J	0 0.10						
κ	0.90 1.00						
L	0.25 0.40						
М	0.10 0.22						
α	0°	8°					
All Di	mensions	in mm					

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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