



#### **60V PNP SMALL SIGNAL TRANSISTOR IN SOT23**

#### **Features**

- Epitaxial Planar Die Construction
- Ideal for Low Power Amplification and Switching
- Complementary NPN Type: MMBT2222A
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP capable (Note 4)

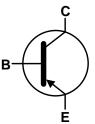
### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)

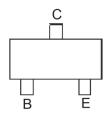




Top View



Device Symbol



Top View Pin-Out

### Ordering Information (Notes 4 & 5)

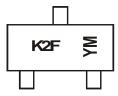
Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMBT2907A-7-F	AEC-Q101	K2F	7	8	3,000
MMBT2907A-13-F	AEC-Q101	K2F	13	8	10,000
MMBT2907AQ-7-F	Automotive	K2F	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_compliance\_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**

SOT23



 $\begin{array}{l} \text{K2F} = \text{Product Type Marking Code} \\ \text{YM} = \text{Date Code Marking} \\ \text{Y or } \overline{\text{Y}} = \text{Year (ex: A} = 2013) \\ \text{M or } \overline{\text{M}} = \text{Month (ex: 9} = \text{September)} \end{array}$ 

Date Code Key

Year	2013		2014	2015		2016	2017		2018	2019		2020
Code	Α		В	С		D	E		F	G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code		_	_	4	-		7	0	٥	0	N	D



## **Absolute Maximum Ratings**(@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	-60	V
Collector-Emitter Voltage	$V_{CEO}$	-60	V
Emitter-Base Voltage	$V_{EBO}$	-6.0	V
Collector Current	Ιc	-600	mA
Peak Collector Current	I <sub>CM</sub>	-800	mA
Peak Base Current	I <sub>BM</sub>	-200	mA

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit	
Collector Power Dissipation	(Note 6)	D <sub>-</sub>	310	mW	
Collector Fower Dissipation	(Note 7)	$P_{D}$	350		
Thermal Resistance, Junction to Ambient	(Note 6)	Ъ	403	°C/W	
Thermal Resistance, Junction to Ambient	(Note 7)	$R_{\theta JA}$	357	C/VV	
Thermal Resistance, Junction to Leads (Note 8)		R <sub>0JL</sub>	350	°C/W	
Operating and StorageTemperatureRange	$T_{J}, T_{STG}$	-55 to+150	°C		

## ESD Ratings (Note 9)

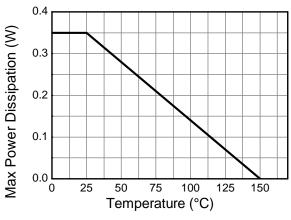
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

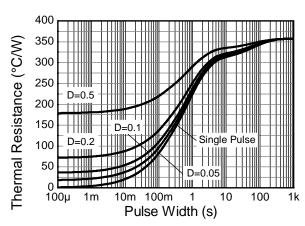
Notes:

- 6. For a device mounted on minimum recommended pad layout 1oz copper that is on a single-sided FR4 PCB; device is measured under still air For a device mounted on minimum recommended pad layout 102 copper that is conditions whilst operating in a steady-state.
  Same as Note 6, except the device is mounted on 15 mm x 15mm 1oz copper.
  Thermal resistance from junction to solder-point (at the end of the leads).
  Refer to JEDEC specification JESD22-A114 and JESD22-A115.



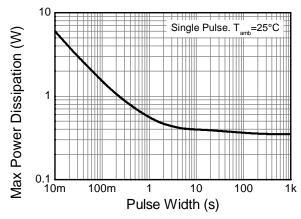
## **Thermal Characteristics and Derating Information**





**Derating Curve** 

**Transient Thermal Impedance** 



**Pulse Power Dissipation** 



## **Electrical Characteristics**(@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS	-				
Collector-Base Breakdown Voltage	$BV_{CBO}$	-60	_	V	$I_C = -100\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 10)	$BV_{CEO}$	-60	_	V	$I_C = -10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$BV_{EBO}$	-6.0		V	$I_E = -100 \mu A, I_C = 0$
Collector Cut-Off Current	I <sub>CBO</sub>		-10	nA	$V_{CB} = -50V, I_E = 0$
Collector Cut-Off Current	la-v		-50	μA nA	$V_{CB} = -50V$ , $I_E = 0$ , $T_A = +125$ °C $V_{CE} = -30V$ , $V_{EB(OFF)} = -0.5V$
Base Cut-Off Current	I <sub>CEX</sub>		-50	nA	, ==(:::)
Emitter Cut-Off Current	I <sub>BL</sub>		-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$
ON CHARACTERISTICS (Note 10)	I <sub>EBO</sub>	_	-50	ΠA	V <sub>EB</sub> = -6.0V
ON CHARACTERISTICS (Note 10)					Ic = -100µA, V <sub>CF</sub> = -10V
		75	_		Ic = -100μA, VcE = -10V Ic = -1.0mA, VcE = -10V
	h	100 100	_		$I_C = -1.0 \text{ MA}, V_{CE} = -10 \text{ V}$ $I_C = -10 \text{ mA}, V_{CE} = -10 \text{ V}$
DC Current Gain	h <sub>FE</sub>	100	300	_	I <sub>C</sub> = -10MA, V <sub>CE</sub> = -10V I <sub>C</sub> = -150mA, V <sub>CE</sub> = -10V
		50	_		Ic = -500mA, VcE = -10V
			-0.4		I <sub>C</sub> = -150mA, I <sub>B</sub> = -15mA
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	_	-1.6	V	Ic = -500mA, I <sub>B</sub> = -50mA
			-1.3		Ic = -150mA, I <sub>B</sub> = -15mA
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	_	-2.6	V	$I_{C} = -500 \text{mA}, I_{B} = -50 \text{mA}$
SMALL SIGNAL CHARACTERISTICS					ic- committee
Output Capacitance	C <sub>obo</sub>	_	8.0	pF	$V_{CB} = -10V$ , $f = 1.0MHz$ , $I_E = 0$
Input Capacitance	C <sub>ibo</sub>	_	30	pF	V <sub>EB</sub> = -2.0V, f = 1.0MHz, I <sub>C</sub> = 0
Current Gain-Bandwidth Product	f⊤	200	_	MHz	$V_{CE} = -20V, I_{C} = -50mA,$ f = 100MHz
SWITCHING CHARACTERISTICS					
Turn-On Time	t <sub>off</sub>	_	45	ns	
Delay Time	t <sub>d</sub>	_	10	ns	$V_{CC} = -30V, I_C = -150mA,$
Rise Time	t <sub>r</sub>	_	40	ns	I <sub>B1</sub> = -15mA
Turn-Off Time	t <sub>off</sub>	_	100	ns	V 0.0V L 450 A
Storage Time	ts	_	80	ns	V <sub>CC</sub> = -6.0V, I <sub>C</sub> = -150mA,
Fall Time	t <sub>f</sub>	_	30	ns	$I_{B1} = I_{B2} = -15 \text{mA}$

Note: 10. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.



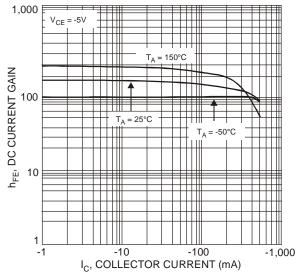


Fig. 1 Typical DC Current Gain vs. Collector Current

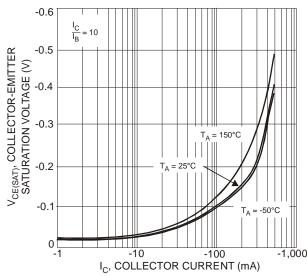


Fig. 3 Typical Collector-Emitter Saturation Voltage vs. Collector Current

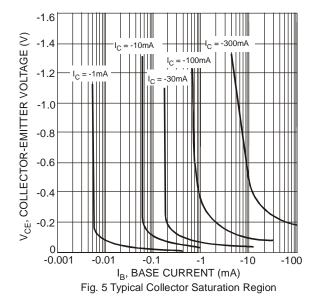


Fig. 2 Typical Base-Emitter Saturation Voltage vs. Collector Current

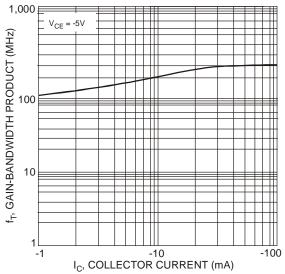


Fig. 4 Typical Gain-Bandwidth Product vs. Collector Current

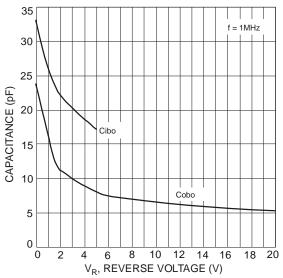


Fig. 6 Typical Capacitance Characteristics

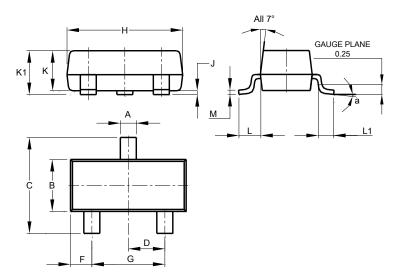
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## **Package Outline Dimensions**

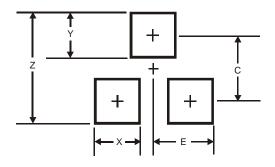
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
а		8°				
All Dimensions in mm						

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
F	1 35

January 2015



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